

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| In re U.S. Patent Application of: |) | Group Art Unit: 2825 |
|-----------------------------------|--------|------------------------------|
| Chong-Min KYUNG |)) | Examiner: A. Thompson |
| Serial Number: 09/760,645 |) | Attorney Docket: KYUN3002beu |
| Filed: January 17, 2001 |) | Confirmation No.: 3451 |

Apparatus And Method For Verifying A Logic Function Of A Semiconductor Chip For:

AMENDMENT AND RESPONSE

Honorable Commissioner For Patents P.O. Box 1450 Alexandria, VA. 22313-1450

Sir:

This paper is in response to the Official Action dated December 18, 2003.

A petition for a two-month extension of time together with the appropriate fees accompanies this response so that it is timely filed.

Amendments to the claims begin on page 2.

Remarks/Arguments begin on page 11.